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PATENT
706316-1019

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#10
Terminal
Disch
4-1-04

Applicant : Beausoleil et al.
Appl. No. : 09/656,541
Filing Date : September 6, 2000
Title : High Speed Software Driven Emulator Comprised Of A Plurality Of Emulation Processors With A Method To Allow High Speed Bulk Read/Write Operation Synchronous DRAM While Refreshing The Memory
Group Art Unit : 2123
Examiner : Ayal I. Sharon
Docket No. : 706316-1019

Mail Stop Fee Amendment
Commissioner for Patents
P.O. Box 1450
Arlington, VA 22313-1450

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MAR 25 2004

Technology Center 2100

TERMINAL DISCLAIMER

Sir:

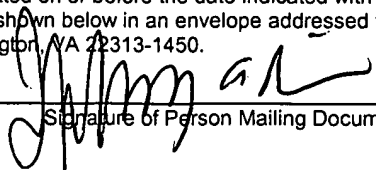
I, Jeffrey A. Miller, represent that I am an attorney of record for the above-identified application. I am authorized to sign this Terminal Disclaimer on behalf of QUICKTURN DESIGN SYSTEMS, INC. I certify that QUICKTURN DESIGN SYSTEMS, INC. owns the entire interest in the above-identified application and invention by virtue of an assignment from the inventors to QUICKTURN DESIGN SYSTEMS, INC. recorded in the Patent and Trademark Office at Reel 6924, Frame 0421-0424. I have reviewed all the documents in the chain of title of

CERTIFICATE OF MAILING
37 CFR §1.8

I hereby certify, pursuant to 37 CFR §1.8, that I have reasonable basis to expect that that this paper or fee (along with any referred to as being attached or enclosed) would be mailed or transmitted on or before the date indicated with the United States Postal Service with sufficient postage as first class mail on the date shown below in an envelope addressed to Mail Stop Non-Fee Amendment, Commissioner for Patents, P.O. Box 1450, Arlington, VA 22313-1450.

Dated: March 17, 2004

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Signature of Person Mailing Document

03/23/2004 AMB11 00000120 150645 09656541 110.00 DA 01 FC:1614

this patent application and, to the best of my knowledge and belief, title and the entire interest of this patent application is held by QUICKTURN DESIGN SYSTEMS, INC.

QUICKTURN DESIGN SYSTEMS, INC. hereby disclaims the terminal part of any patent granted on the above-identified application that would extend beyond the statutory expiration date of U.S. Patent No. 6,618,698. QUICKTURN DESIGN SYSTEMS, INC. is the owner of U.S. Patent No. 6,618,698 by virtue of assignment from the inventors to QUICKTURN DESIGN SYSTEMS, INC.

Moreover, QUICKTURN DESIGN SYSTEMS, INC. hereby agrees that any patent so granted on the above-identified application shall be enforceable only for and during such period that the legal title to said patent shall be the same as the legal title to U.S. Patent No. 6,618,698, this agreement to run with any patent granted on the above-identified application and to be binding upon QUICKTURN DESIGN SYSTEMS, INC., its successors, or assigns.

QUICKTURN DESIGN SYSTEMS, INC. does not disclaim any terminal part of any patent granted on the above-identified application prior to the expiration date of the full statutory term of U.S. Patent No. 6,618,698, in the event that this patent later expires for failure to pay a maintenance fee, is held unenforceable, is found invalid, is statutorily disclaimed in whole or terminally disclaimed under 37 C.F.R. §1.321(a), has all claims cancelled by a reexamination certificate, or is otherwise terminated prior to expiration of its statutory term, except for the separation of legal title stated above.


Method of Payment of fees:

- ☐ A check in the amount of _____ is enclosed to cover the above fee(s).
- ☒ Charge Orrick's Deposit Account No. **15-0665** in the amount of **\$110.00**.
- ☒ The Commissioner is authorized to charge Orrick's Deposit Account No. **15-0665** for any fees required under 37 CFR §§ 1.16, 1.17 and 1.445 that are not covered, in whole or in part, by a check enclosed herewith and to credit any overpayments to said Deposit Account No. **15-0665**.

Respectfully submitted,

ORRICK, HERRINGTON & SUTCLIFFE LLP

Dated: March 17, 2004



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